# Introduction to Performance Tools

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### Goals of the Breakout Session

- Let you know what tools are available at OSC
- Suggest when you should use each of them
- Give an overview of usage for each
  - Including a demo or sample output
- Show you where to find more information



### For More Information

 Visit the software pages on our website <u>www.osc.edu</u> Resource → Available Software

- Contact the help desk (OSC Help) oschelp@osc.edu 614-292-1800 1-800-686-6472
- Optimization and Performance Tuning Workshop on May 21, 2019 at 1-4 pm osc.edu/calendar/events/2019\_05\_21-optimization\_

performance\_tuning\_workshop



Profiling/Debugging Tools Available at OSC

- Parallel debugging tools
  - ARM DDT
- Profiling tools
  - ARM Performance Reports
  - ARM MAP
  - Intel VTune
  - Intel Trace Analyzer and Collector (ITAC)
  - Intel Advisor
  - TAU Commander
  - HPCToolkit



### What can a debugger do for you?

### Debuggers let you

- execute your program one line at a time ("step")
- inspect variable values
- stop your program at a particular line ("breakpoint")
- open a "core" file (after program crashes)
- HPC debuggers
  - support multithreaded code
  - support MPI code
  - support GPU code
  - provide a nice GUI



# Compilation flags for debugging

For debugging:

- Use -g flag
- Remove optimization or set to -00
- Examples:
  - icc -g -o mycode mycode.c
  - ▶ gcc -g -OO -o mycode mycode.c
- Use icc -help diag to see what compiler warnings and diagnostic options are available for the Intel compiler
- Diagnostic options can also be found by reading the man page of gcc with man gcc



### ARM DDT

Available on all OSC clusters

module load arm-ddt

To run a non-MPI program from the command line:

ddt --offline --no-mpi ./mycode [args]

- ▶ To run a MPI program from the command line:
  - ddt --offline -np num\_procs ./mycode [args]



# ARM DDT GUI

- To run ARM DDT as a GUI, login to OnDemand at ondemand.osc.edu
- To get an interactive session on a compute node, select "Pitzer Desktop" under "Interactive Apps"
- Enter information and click "Launch"
- Click "Launch noVNC in New Tab" to launch the desktop in a new tab
- From there you can open a terminal and run DDT as a GUI
- For a non-MPI program:
  - ddt --no-mpi ./mycode [args]
- For a MPI program:

ddt -np num\_procs ./mycode [args]

More information on using OnDemand is available at osc.edu/resources/online\_portals/ondemand



# OnDemand Screenshot

### Ruby Desktop Owens VDI Pitzer VDI Ruby VDI aUIs ANSYS Workbench

Desktops

Owens Desktop

Pitzer Desktop

Abagus/CAE

COMSOL Multiphysics

- 📣 MATLAB
- ParaView
- vmD

Servers

Jupyter + Spark

🚎 Jupyter Notebook

Jupyter Notebook (Pitzer)

RStudio Server

BStudio Server (Pitzer)

#### Pitzer Desktop

This app will launch an interactive desktop on one or more compute nodes. You will have full access to the resources these nodes provide. This is analogous to an interactive batch job.

#### Desktop environment

Xice This will launch either the Xice or Mate desktop environment on the Pitzer cluster.

#### Account

You can leave this blank if not in multiple projects.

#### Number of hours

#### Number of nodes

1		
lode type		
any		

- any (40 cores) Chooses anyone of the available Pitzer nodes. This reduces the wait time as you have no requirements. Standard Pitzer nodes have 192GB of memory.
- vis (40 cores) This requests a gpu node as described below, with the addition that it starts an X server running in the background. This allows for Hardware Rendering with the GPU typically needed for 3D visualization using VirtualQL.
- gpu (40 cores) This node includes two NVIDIA Tesla V100 GPUs allowing for CUDA computations. This node has 384GB of memory. Three are currently only 32 of these nodes on Pitzer. These nodes don't start an X server, so visualization with hardware rendering is not possible.
- hugemem (80 cores) This Pitzer node has 3TB of memory as well as 80 cores. There are only 4 of these nodes on Pitzer. A reservation may be required to use this node.

#### Resolution



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### ARM DDT

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### What can a profiler show you?

### Whether code is

compute-bound

- memory-bound
- communication-bound
- How well the code uses available resources
  - Multiple cores
  - Vectorization

How much time is spent in different parts of the code



Compilation flags for profiling

For profiling

- Use -g flag
- Explicitly specify optimization level -On
- Example: icc -g -03 -o mycode mycode.c
- Use the same level optimization as you normally do
  - Bad example: icc -g -o mycode mycode.c
    - Equivalent to -00

### **ARM Performance Reports**

Easy to use

- "-g" flag not needed works on precompiled binaries
- Gives a summary of your code's performance
  - view report with browser
- For a non-MPI program:
  - module load arm-pr
  - perf-report --no-mpi ./mycode [args]
- For an MPI program:
  - perf-report -np num\_procs ./mycode [args]



ORMANCE REPORTS	Command:	/fs/project/PZS0720/skhuvis/SETSM/setsm dataset/WV01_15MAV080613301- P185-102001030202A600.tif dataset/WV01_15MAY080614188- P185-102001003EA5DA00.tif out -outres 8 -projection ps	Compute
	Resources:	1 node (40 physical, 40 logical cores per node)	MPI
	Tasks:	1 process, OMP_NUM_THREADS was 28	
	Machine:	p0165.ten.osc.edu	
	Start time:	Fri Dec 28 2018 14:13:20 (UTC-05)	
	Total time:	372 seconds (about 6 minutes)	
	Full path:	/fs/project/PZS0720/skhuvis/SETSM	

### Summary: setsm is Compute-bound in this configuration

Compute	99.3%	Time spent running application code. High values are usually good. This is <b>very high</b> ; check the CPU performance section for advice
MPI	0.0%	Time spent in MPI calls. High values are usually bad. This is <b>very low</b> ; this code may benefit from a higher process count
I/O	0.7%	Time spent in filesystem I/O. High values are usually bad. This is <b>very low</b> ; however single-process I/O may cause MPI wait times

This application run was Compute-bound. A breakdown of this time and advice for investigating further is in the CPU section below.

As very little time is spent in MPI calls, this code may also benefit from running at larger scales.

### CPU

A breakdown of the 99.3% CPU time:



#### MPI

A breakdown of the 0.0% MPI time		
Time in collective calls	0.0%	T.
Time in point-to-point calls	0.0%	I.
Effective process collective rate	0.00 bytes/s	I.
Effective process point-to-point rate	0.00 bytes/s	I.

No time is spent in MPI operations. There's nothing to optimize here!

The per-core performance is memory-bound. Use a profiler to identify time-consuming loops and check their cache performance.

Little time is spent in vectorized instructions. Check the compiler's vectorization advice to see why key loops could not be vectorized.



### I/O



Most of the time is spent in read operations with a high effective transfer rate. It may be possible to achieve faster effective transfer rates using asynchronous file operations.

### Memory

Per-process memory usage may also affect scaling:

Mean process memory usage 1.16 GiB Peak process memory usage 3.70 GiB Peak node memory usage 8.0%

The peak node memory usage is very low. Larger problem sets can be run before scaling to multiple nodes.

#### OpenMP

A breakdown of the 55.5% time in OpenMP regions: Computation 78.5% Synchronization 21.5% Physical core utilization 70.0% System load 57.9%

OpenMP thread performance looks good. Check the CPU breakdown for advice on improving code efficiency.

### Energy

A breakdown of how the 19.1 Wh was used:

CPU	100.0%	
System	not supported %	1
Mean node power	not supported W	1
Peak node power	0.00 W	1

The whole system energy has been calculated using the CPU energy usage.

System power metrics: No Arm IPMI Energy Agent config file found in /var/spool/ipmi-energy-agent. Did you start the Arm IPMI Energy Agent?



### ARM MAP

- Interpretation of profile requires some expertise
- Gives details about your code's performance
- For a non-MPI program:
  - module load arm-map
  - map --profile --no-mpi ./mycode [args]
- For an MPI program:
  - map --profile -np num\_procs ./mycode [args]
- View and explore resulting profile using ARM client
  - Download remote client to view profiles on local machine at developer.arm.com/products/ software-development-tools/hpc/downloads/ download-arm-forge
  - Information on transferring files to your local machine at osc.edu/resources/online\_portals/ondemand/file\_ transfer\_and\_management







#### Input/Output Project Ples OperMP Stacks OperMP Regions Functions

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Showing data from 1.000 samples taken over 1 process (1000 per process)

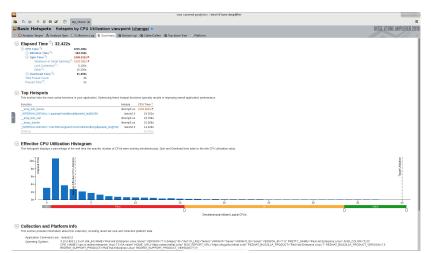


### Intel VTune

- ▶ A profiler that can work with C, C++, Fortran programs
- Works best on a single node
- ▶ For using a GUI (use for small problems < 5 minutes):
  - ▶ amplxe-gui
- For non-interactive usage:
  - amplxe-cl -r my\_vtune -collect hotspots -no-auto-finalize ./mycode
  - amplxe-cl -report hotspots -r my\_vtune
- View and explore existing results with amplxe-gui



### VTune GUI





Intel Trace Analyzer and Collector (ITAC)

- Graphical tool for profiling MPI code (Intel MPI)
- To use:
  - module load intelmpi # then compile (-g) code
  - mpiexec -trace ./mycode
- View and explore existing results using GUI with traceanalyzer:
  - traceanalyzer <mycode>.stf

# ITAC GUI

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# **TAU** Commander

- Tool that can be used to profile, trace, or sample your application
- Load with taucmdr module
- Requires moderate amount of setup:
  - Create a project with information about the application
    - For example, tau initialize --mpi --compilers Intel
  - Select appropriate measurement:
    - tau select sample
- To use:
  - tau mpiexec ./mycode
- To view and explore profile data:
  - tau trial show <trial\_number>



### **TAU** Profile

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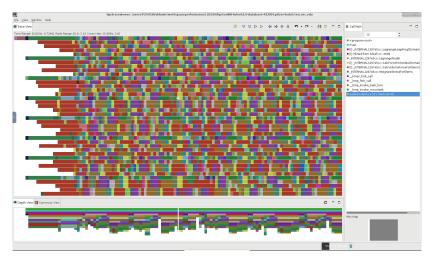
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# **HPCtoolkit**

- Suite of tools that can be used to profile or trace your application
- Load with hpctoolkit module
- ▶ To profile your application: mpiexec hpcrun ./mycode
- This will produce a directory with a name of the form hpctoolkit-mycode-measurements-pid.nodeid containg profile data
- To convert the output to a format than be viewed by the hpcviewer tool, run hpcprof hpctoolkit-mycode-measurements-pid.nodeid
- To view the profile data generated during the run in a GUI, call hpcviewer hpctoolkit-mycode-database-pid.nodeid

# **HPCToolkit** Profile





### Intel Advisor

Graphical tool for optimizing vectorization and threading

▶ For using a GUI (use for small problems < 5 minutes):

advixe-gui

- For non-MPI non-interactive usage:
  - advixe-cl -collect survey -project-dir ./my\_advisor ./mycode
- ► For MPI non-interactive usage:
  - mpirun -n <mpi\_tasks> advixe-cl -collect survey -project-dir ./my\_advisor ./mycode
- View and explore existing results with advixe-gui



### Intel Advisor GUI

le View Help	/uverx/PZ549530/ukhuvis/workspace/performancei1.ULE544my_advisor - Intel Advisor	(* o
	vev Analysis 🗉 🚅 🕅	
elcome rank 0 16		
Vectorization Threading Workflow	Express Trive 63.975     Sectional Control Contro	INTEL ADVISOR 2
orr 📗 Ratch mode	Vectorization Advisor	
Run Rooffine	Vectorization Advisor is a vectorization analysis tooliset that lets you identify loops that will benefit most from vector parallelism, discover performance issues preventing from effective vectorization and characterize your memory vs. vectorization bottlenecks with Advisor Rooffne	
🕨 Collect 💼 💽	per unitation rices preventing num encode vecchaatan and crasiscosiae your mentaly is: exciduation scorenects with austat advante model automation.	
Enable Roofline with Calistacks	Program metrics     Exposed Time 63.975	
. Survey Target	Vector Instruction Set. Anv. SSE2, SSE Number of CPU Threads 5	
G Collect 🔥 🖿 🗔	Soop metrics	
lark Loops for Deeper Analysis	Metrics Total Total CPU Dine 296.825 106.06	
elect checkboxes in the Survey & endine tab to mark loops for other	Time in 28 vectorized loops 15.04s 5.1%	
dvisor analyses. There are no marked loops	Time in scalar code 201.785 64.9% Including time in 3 vectored complete unrolled loops 0	
1 Find Trip Counts and FLOP		
🖓 Collect 🖡 🖿 🗔	Vectorization Gain/Efficiency     Vectorization Gain/Efficiency     Vectorization Gain/Efficiency	
✓ Tip Counts	Program Approximate Gain 0 1.03x	
FLOP	⊙ Per program recommendations	
2.1 Check Memory Access Patterns	Aligher instruction set architecture (ISA) available     Consider recorpiling your application using a higher ISA. Stop mare	
🖟 Collect 💼 🔛		
- No loops selected	top time-censuming loops <sup>1</sup> Loop Set Time <sup>0</sup>	
2.2 Check Dependencies	O Foop in _NTERNAL1297e5cc:Lagrangehistationp tografiel_for@1009 at lutesh.cc:10101 5.847s 5.847s	
No loops selected	O Toop n_NTERVALIZZETCC_GASTRHscr@artificrestoreignmarkht/freeZ22 at latesh.cc.2281 2.450s     Toop n_NTERVALIZZETCC_GASTRHscr@artificrestoreignmarkht/freeZ222 at latesh.cc.2891 2.450s     Toop n_NTERVALIZZETCC_GASTRHScr@artificrestoreignmarkht/freeZ222 at latesh.cc.2891 2.450s     Sortes	
	Joop in INTERNALL297e5cc: CalcF8HaurglassFarceFarEtenstomptparallel (ori8969 or Lidenb.cc;972) 2.670s     Joop in INTERNAL297e5cc: CalcF8HaurglassFarceFarEtenstomptparallel (ori8782 at Lidenb.cc;782) 2.205s     9.501s	
	Recommendations <sup>1</sup>	
	Losp Self Trine® Recorresentations®	
	oj foop in _https/slil297esicc:SekrBHzerglassFerceFertiens.formp.fparallel.forB782 at Jakah.cc;783) 3,4505 💡 Spit loop.nto smaller loops	
	Soliection details	
	○ Platform information	
	Metrizanic o CPV Name Intel[RI Xeen(RI Gold 8149 CPU #9.2.4901t2	
	Frequency 2.40 DHz	
	Logical CPU Count 40 Operating System Linux	
	Computer Name p021.8 ten.osc.edu	
G Re-finalize Sur		



Resources to get your questions answered

FAQs: osc.edu/resources/getting\_started/supercomputing\_faq HOW TOs: osc.edu/resources/getting\_started/howto

Performance Collection Guide: osc.edu/resources/getting\_started/howto/howto\_collect\_ performance\_data\_for\_your\_program

Office Hours: go.osu.edu/rc-osc Tuesdays 1-3 p.m. or Wednesdays and Fridays 1-2:30 p.m. at Pomerene Hall

System updates:

- Read Message of the Day on login
- Follow @HPCNotices on Twitter



### Optimization and Performance Tuning Workshop

- May 21, 2019 at 1-4 pm
- Present techniques for improving the performance of scientific software on High Performance Computing (HPC) systems such as those available at OSC.
- The focus will be on serial performance, including vectorization and cache utilization, with a brief mention of parallel computing.
- Topics covered:
  - Hardware overview
  - Important factors for good performance
  - Compiler optimizations
  - Profiling tools

osc.edu/calendar/events/2019\_05\_21-optimization\_
performance\_tuning\_workshop





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